

Unbuffered divider.

Figure 1

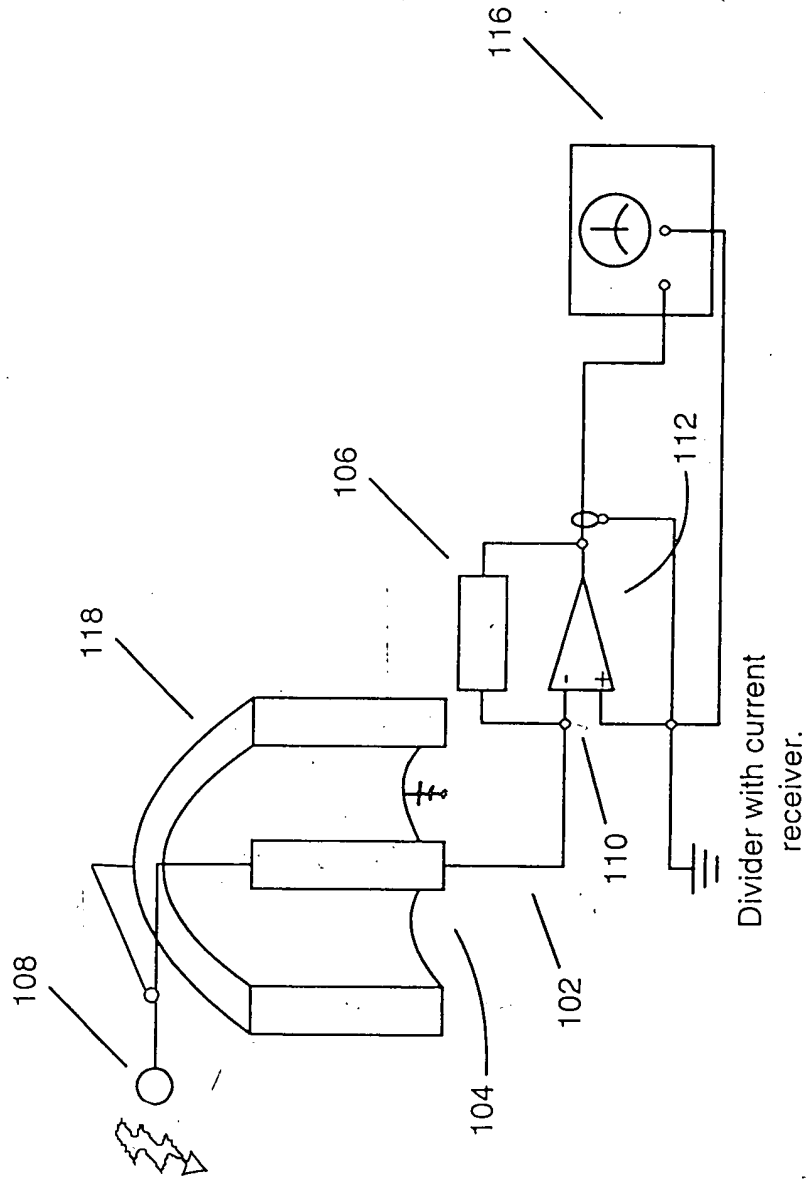


Figure 2

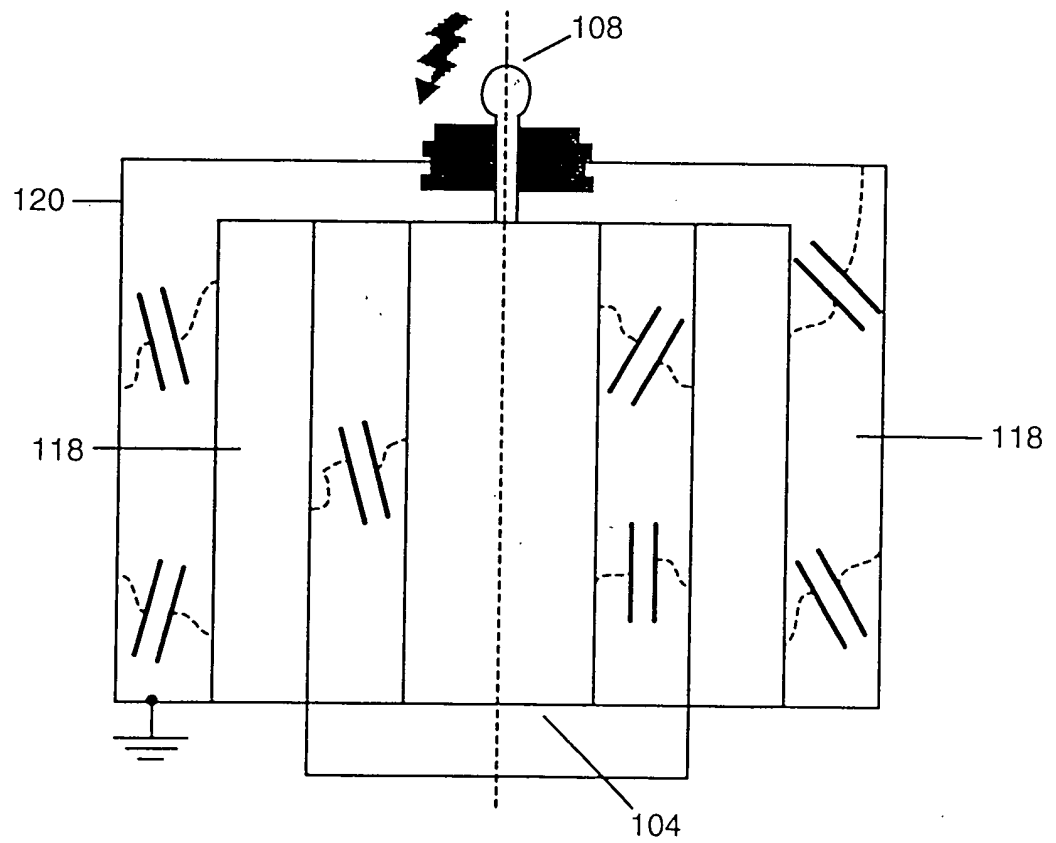


Figure 3



**PARAMETERS:**

$C_g = 10n$

$C_{sd} = 1.37p$

$C_{sg} = 0.80p$

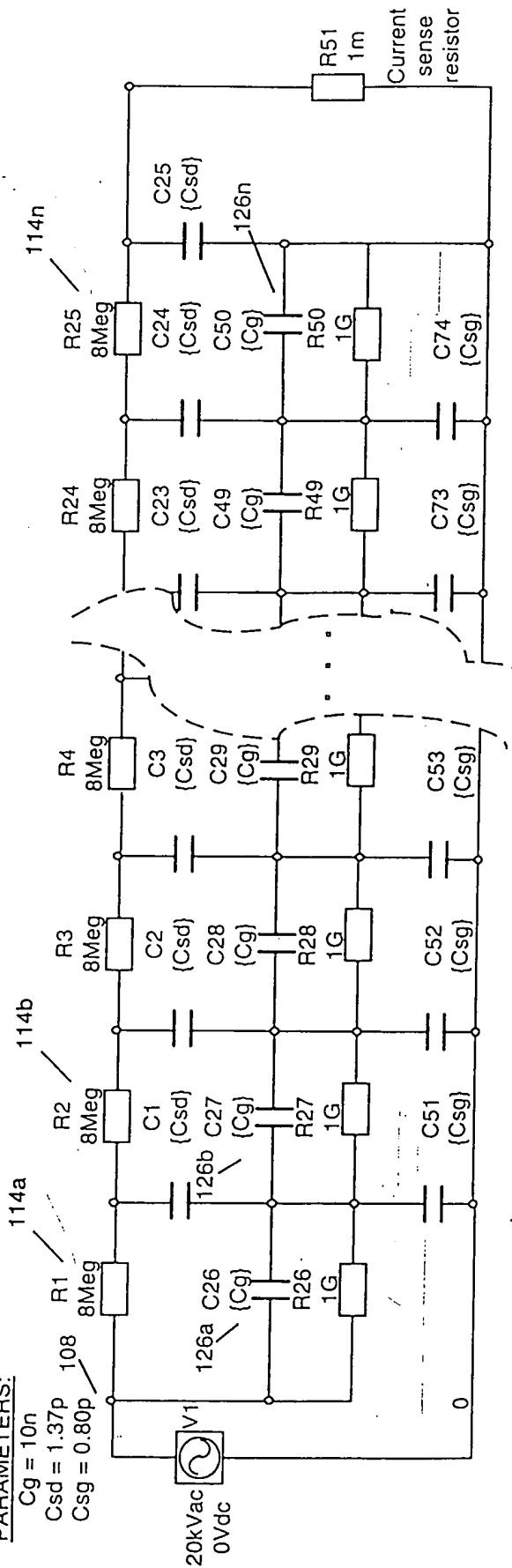


Figure 4

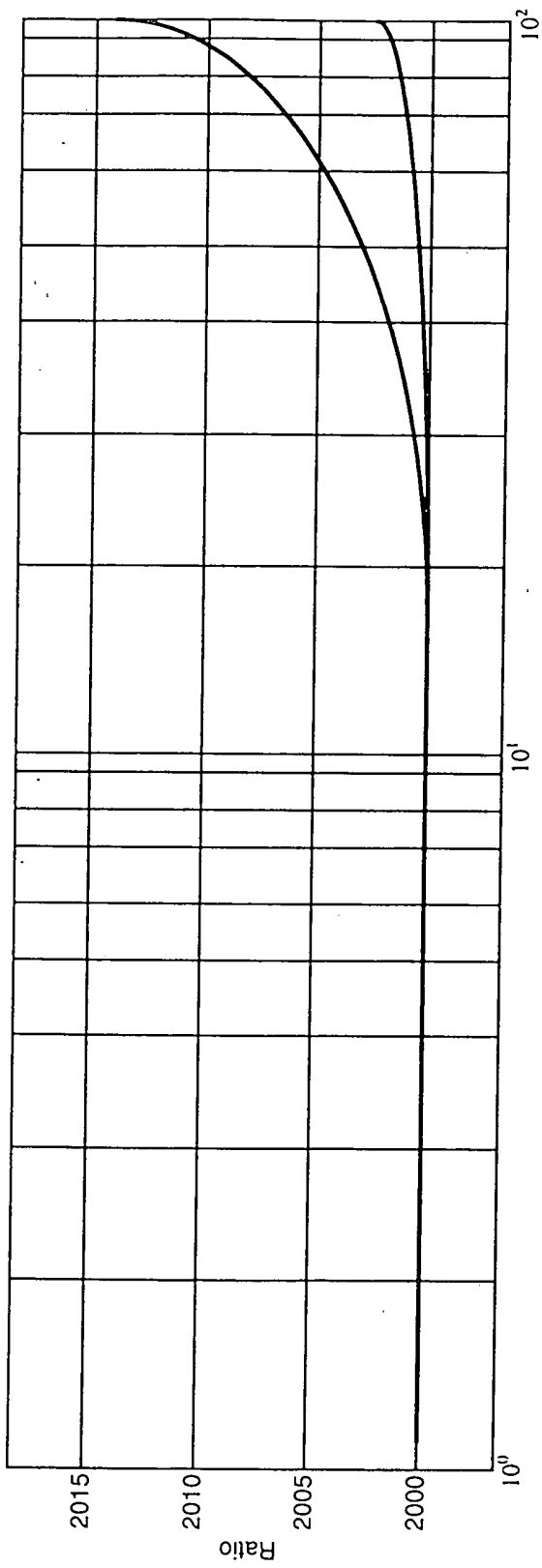


Figure 5

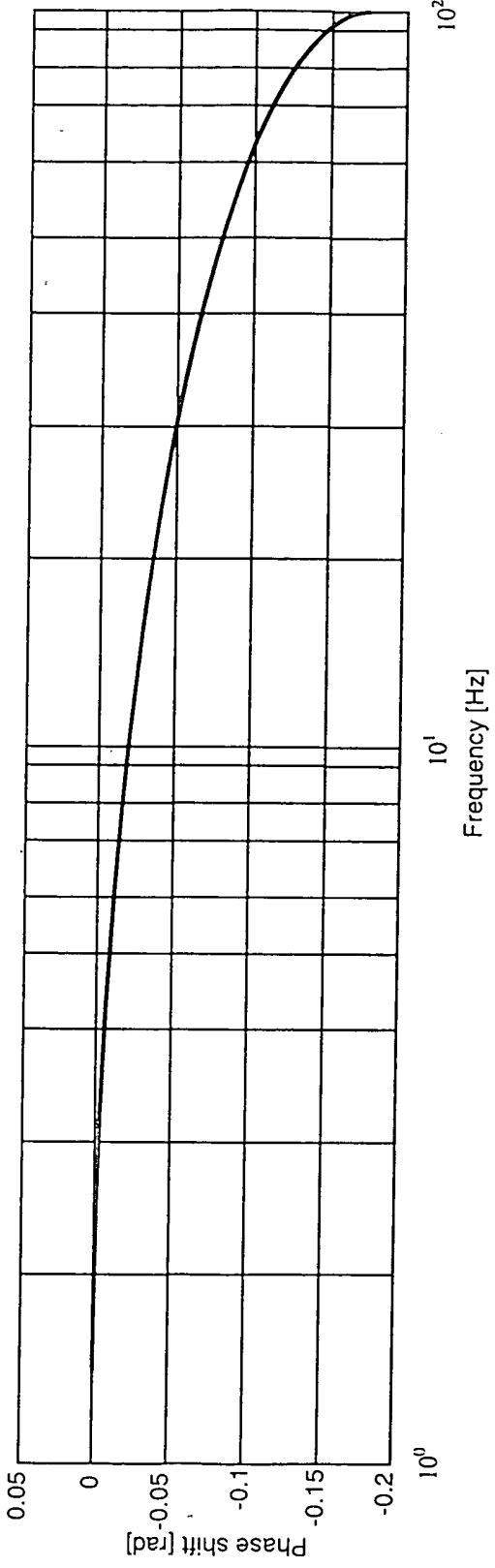


Figure 6

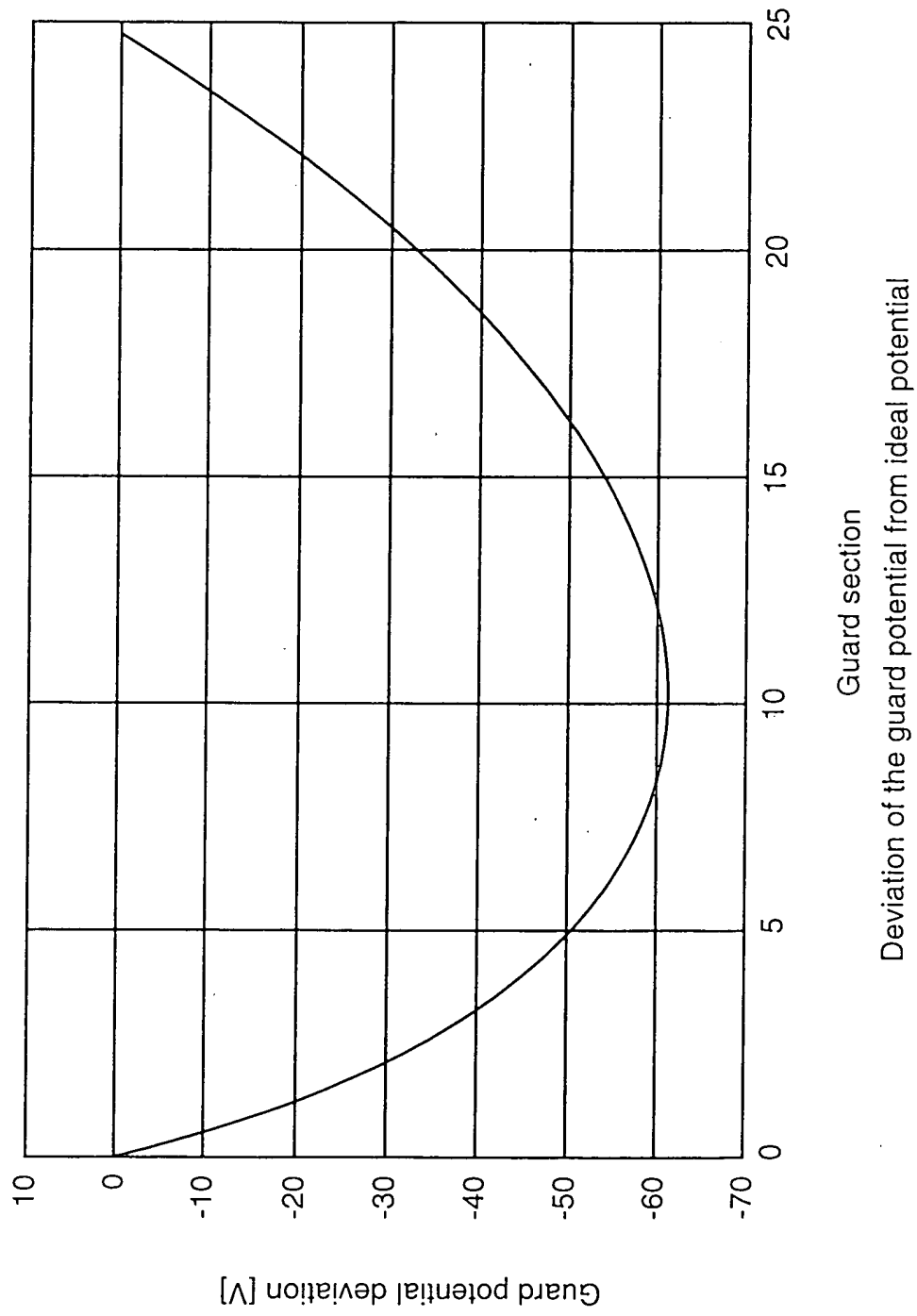


Figure 7

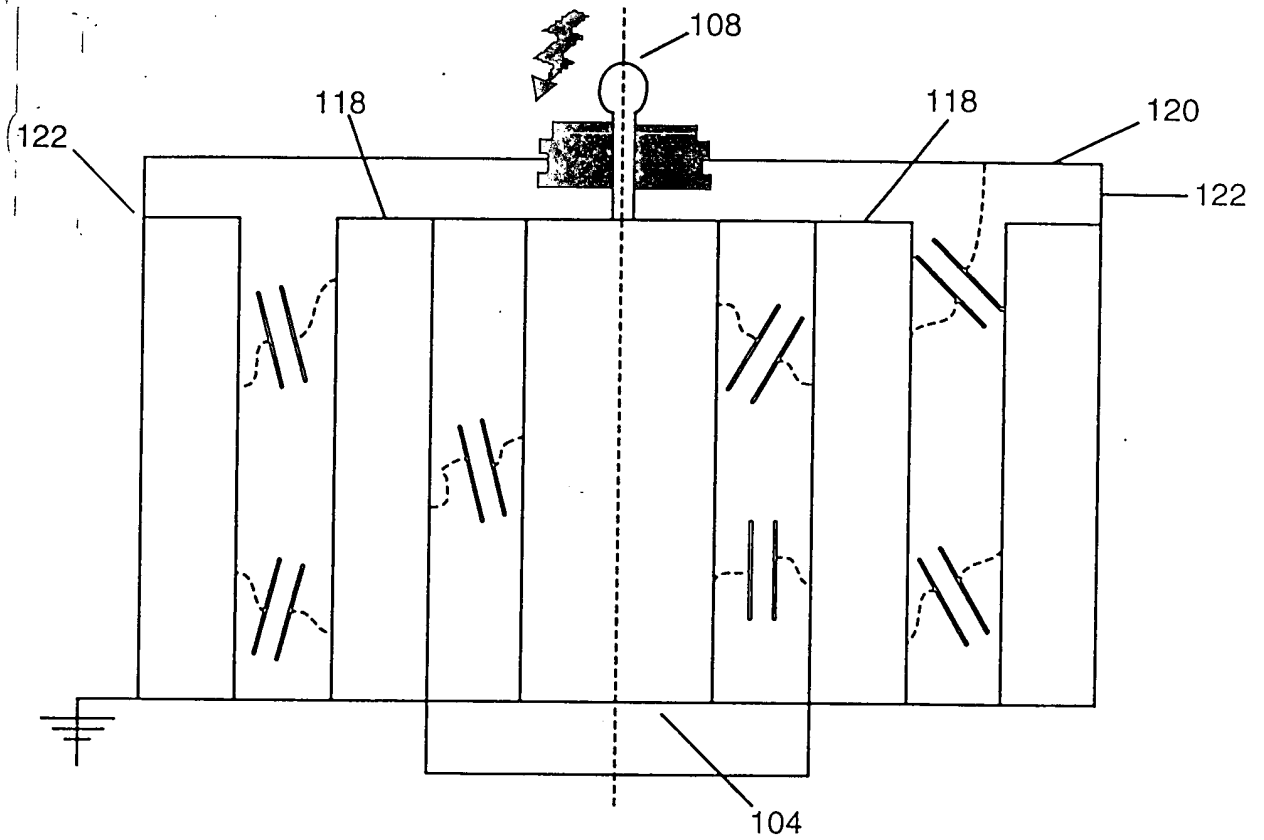


Figure 8



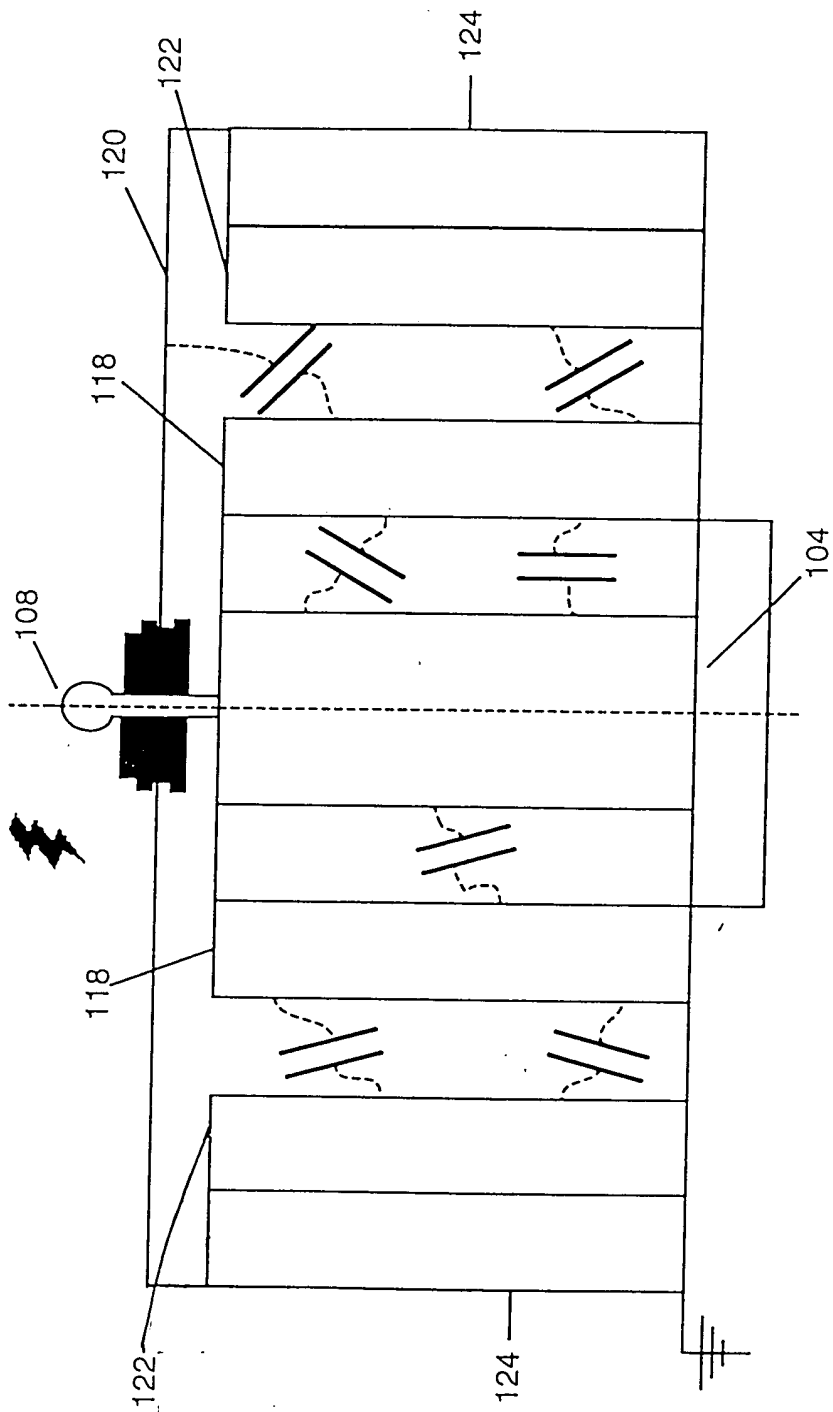


Figure 9



**PARAMETERS:**

$C_g = 5n$

$C_{sd} = 1.37p$

$C_{sg} = 1.09p$

$C_{si} = 2.49p$

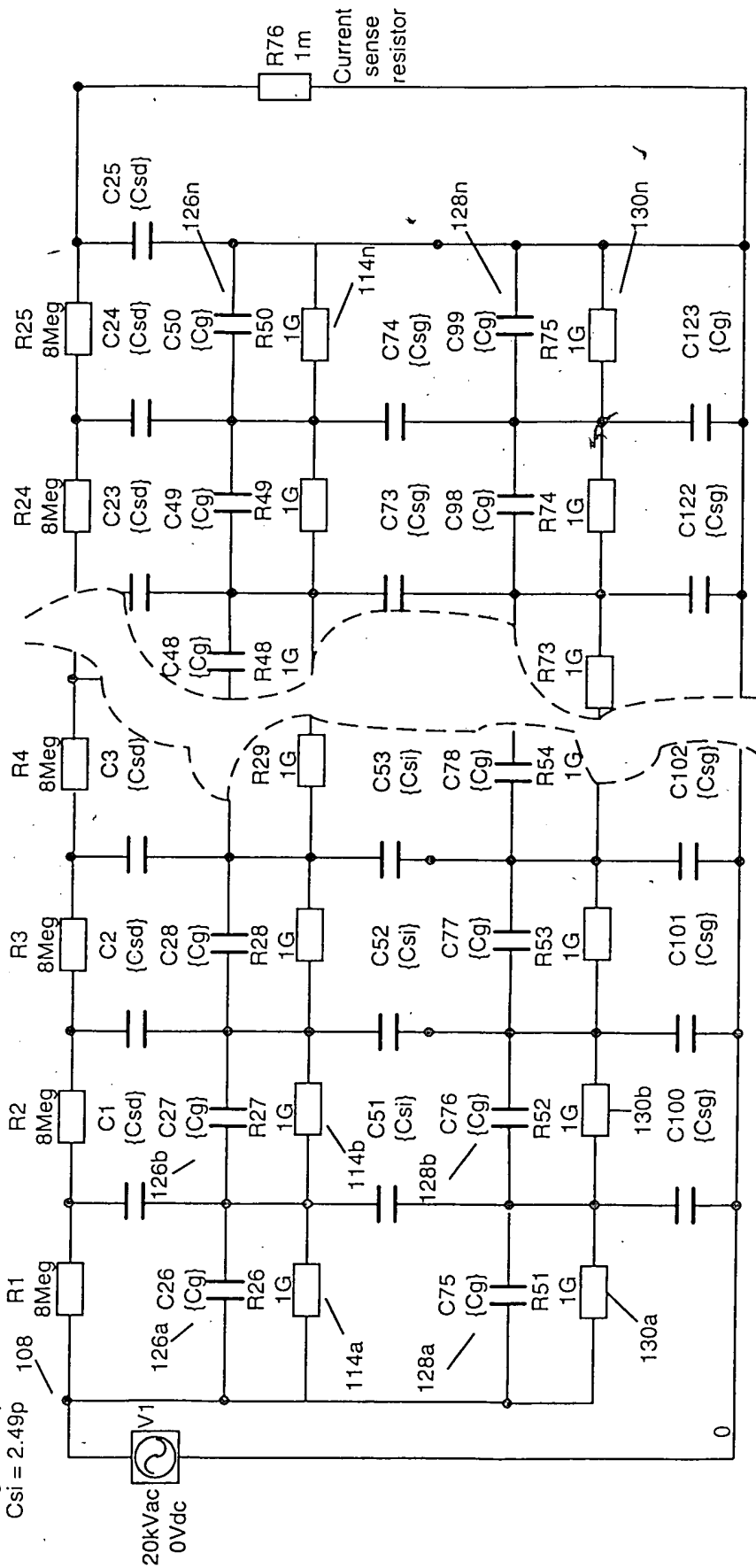
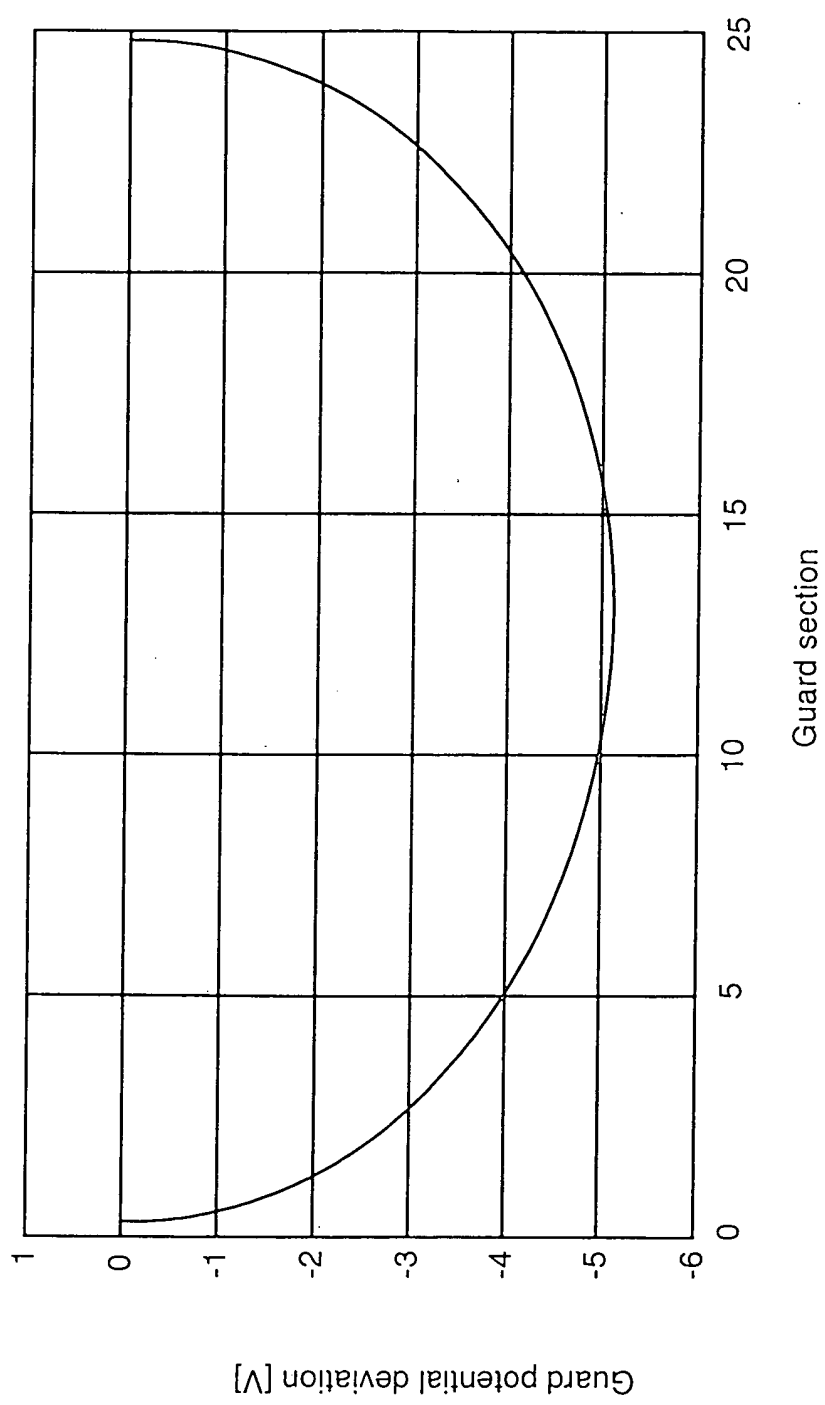
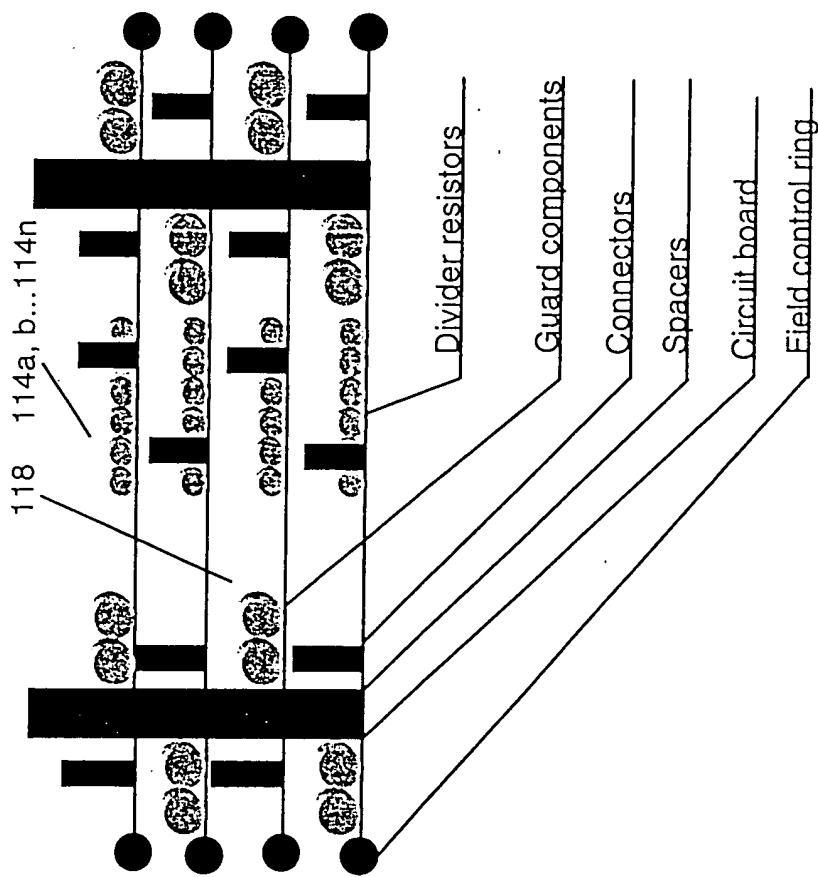


Figure 10



Deviation of the guard potential from ideal potential

Figure 11



Cross-sectional view of four  
circuit boards in a divider stack

Figure 12

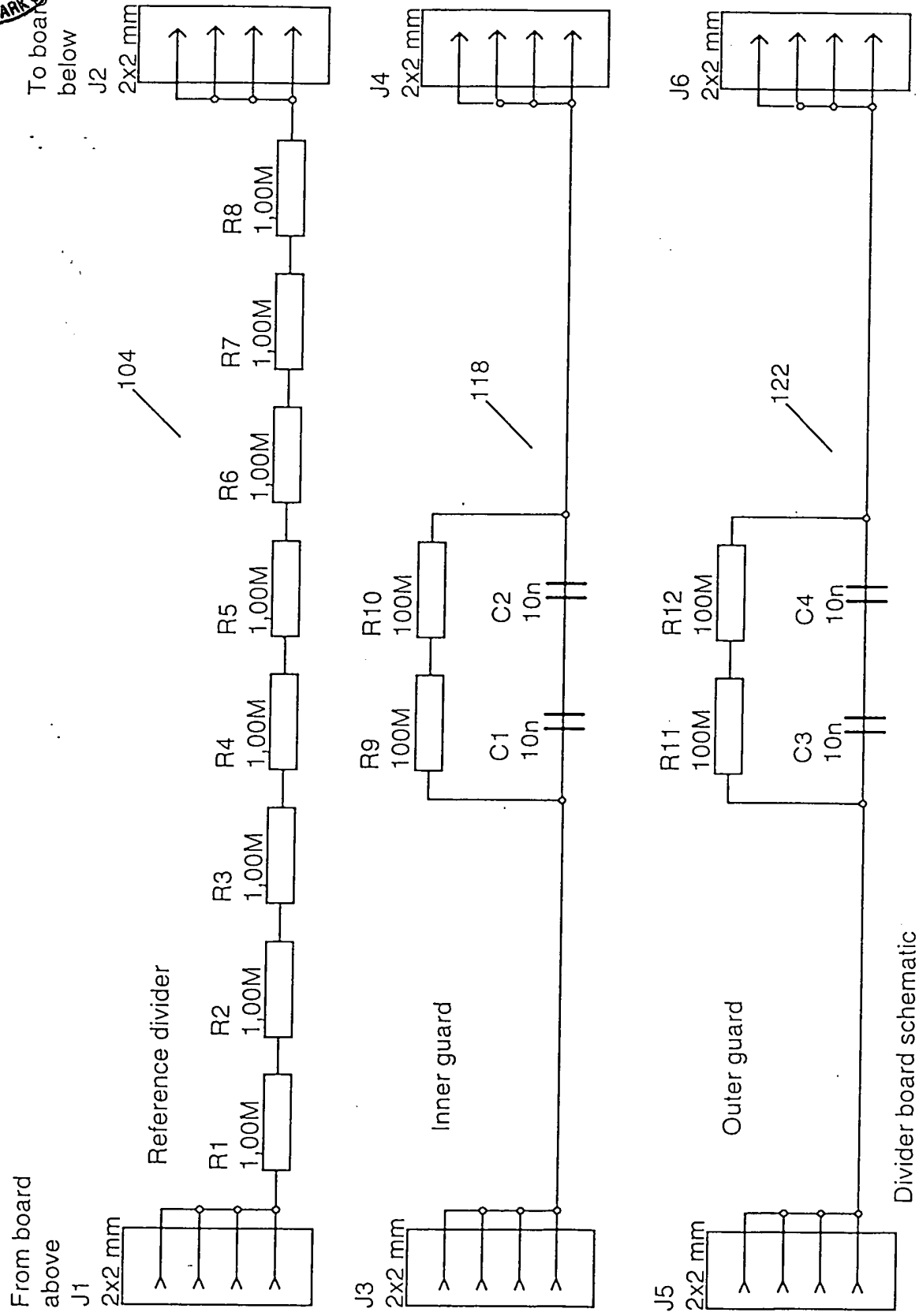


Figure 13

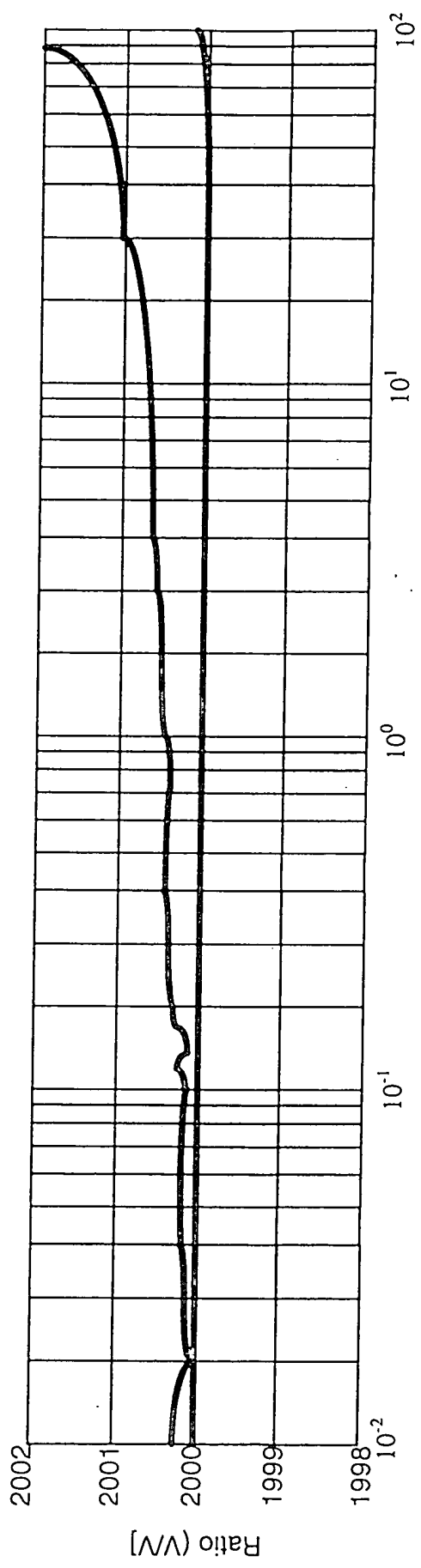


Figure 14

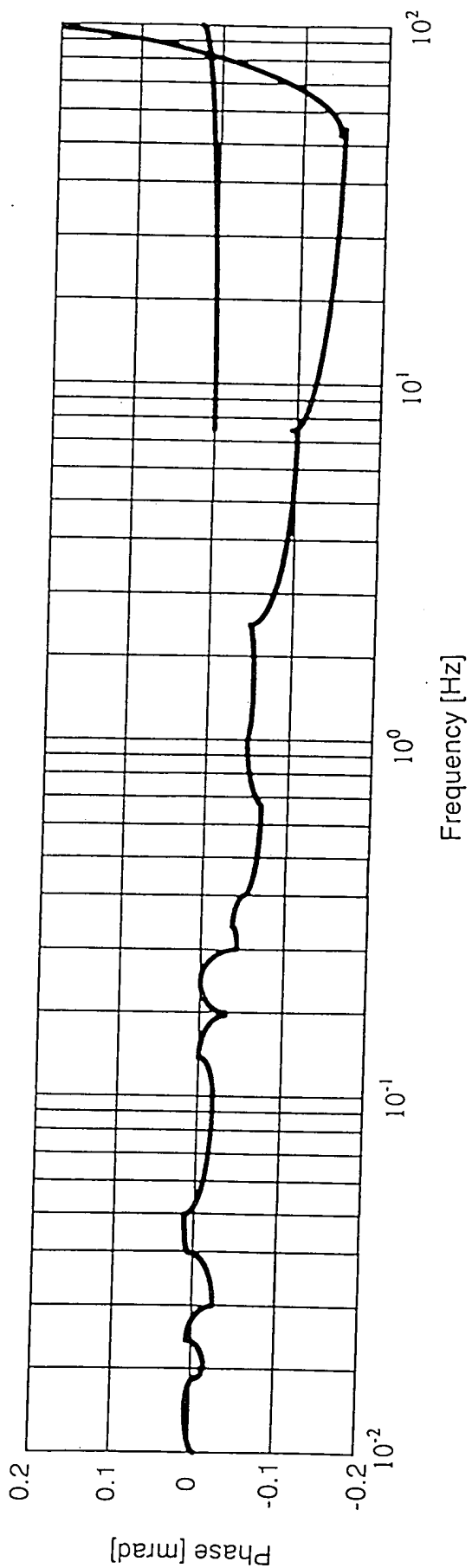


Figure 15

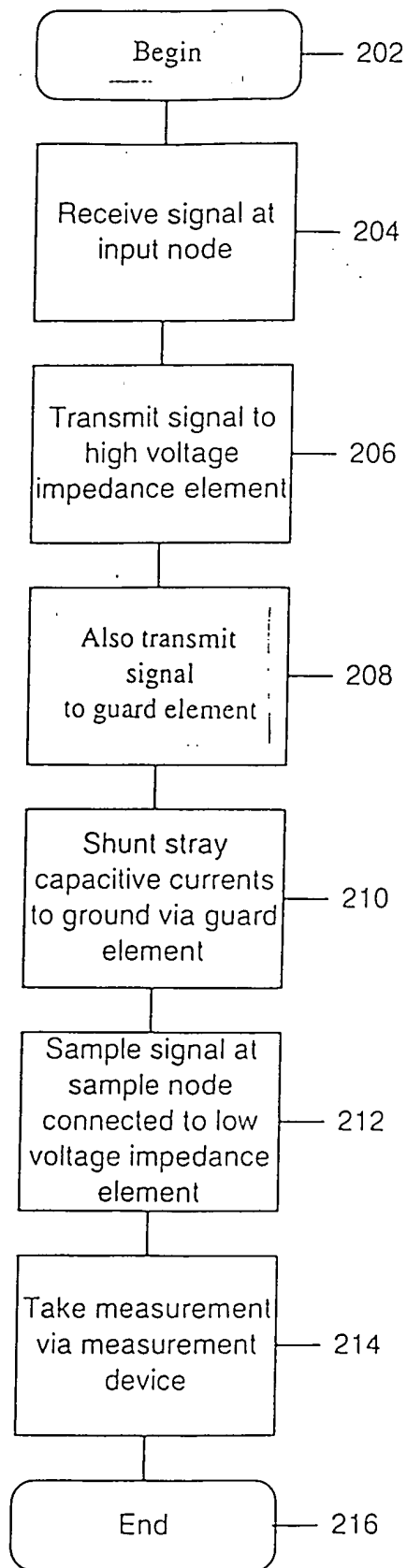
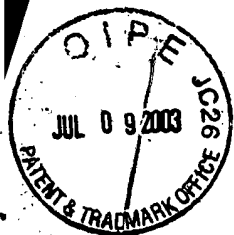


Figure 16